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E. Leiserson**

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Charles E. Leiserson received a BS from Yale University in Professor Leiserson s research centers on the theory of parallel Area-Efficient VLSI

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Charles E. Leiserson. MIT. Proceedings of the fourth annual ACM symposium on Parallel algorithms Third Caltech Conference on Very Large Scale Integration, 87

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By Charles Leiserson. Virtual Memory for Data-Parallel Proceedings of the MIT Student Workshop on VLSI and Parallel Systems Held in Dedham, Massachusetts on

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Theory of Parallel Hardware; Prof. Charles E. Leiserson, MIT EECS and CSAIL Prof. Michael Bender, The class is suitable for both theory and systems students.

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Vlsi theory and parallel supercomputing. MIT/LCS/TM 402, MIT, 545 Technology Sq (1989)

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